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STATEMENT BY APPLICANT

Complete If Known

Application Number	10/806,917
Filing Date	03-22-2004
First Named Inventor	Dimpsey et al.
Examiner Name	Unknown - Ben C. Wang
Art Unit	2122- 2192
Attorney Docket No.	AUS920040063US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
BW AA	5,710,881	01-20-1998	Gupta et al.	
BW AB	5,822,763	10-13-1998	Baylor et al.	
BW AC	5,928,334	07-27-1999	Mandyam et al.	
BW AD	6,094,709	07-25-2000	Baylor et al.	
BW AE	6,285,974 B1	09-04-2001	Mandyam et al.	
BW AF	6,636,950 B1	10-21-2003	Mithal et al.	
BW AG	2004/0205302 A1	10-14-2004	Cantrill	

FOREIGN PATENT DOCUMENTS

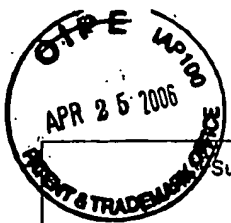
Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

NON PATENT LITERATURE

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
BW AH	TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 nd Edition, pp. 10-12.
BW AI	TORRELLAS et al., "False Sharing and Spatial Locality in Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.
BW AJ	ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.

Examiner: /Ben Wang/ Date Considered: 04/04/2007

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
BW AA	5,797,019	08-18-1998	Levine et al.	
BW AB	6,549,998 B1	04-15-2003	Pekarich et al.	
BW AC	2003/0101367 A1	05-29-2003	Bartfal et al.	
BW AD	5,113,507	05-12-1992	Jaeckel	
BW AE	6,681,387 B1	01-20-2004	Hwu et al.	
BW AF	6,925,424 B2	08-02-2005	Jones et al.	

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BW AG	Wikipedia "JavaServer Pages" downloaded 1/24/06 http://en.wikipedia.org/wiki/JavaServer_Pages
BW AH	"Hardware Cycle Based memory Residency", IBM, May 22, 2003, ip.com, IPCOM000012728D, pp. 1-2
BW AI	HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html

Examiner: /Ben Wang/

Date Considered: 04/04/2007

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**INFORMATION DISCLOSURE
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Application Number 10/806,917
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 First Named Inventor Dimpsey et al.
 Examiner Name Dan Tuan Quang Ben C. Wang
 Art Unit 2192
 Attorney Docket No. AUS920040063US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
BW AA	5394529	02-28-1995	Brown, III et al.	
BW AB	6233679 B1	05-15-2001	Holmberg	
BW AC	5887159	03-23-1999	Burrows	
BW AD	6662295 B2	12-09-2003	Yamaura	
BW AE	5938760	08-17-1999	Levine et al.	
BW AF	6189072 B1	02-13-2001	Levine et al.	
BW AG	5276833	01-04-1994	Auvinen et al.	
BW AH	5287481	02-15-1994	Lin	
BW AI	5537572	07-16-1996	Michelsen et al.	
BW AJ	5805879	09-08-1998	Hervin et al.	
BW AK	6647301 B2	11-11-2003	Sederlund et al.	
BW AL	6735666 B1	05-11-2004	Koning	
BW AM	2004/0153612 A1	08-05-2004	Mutz et al.	
BW AN	2005/0091456 A1	04-28-2005	Huck	
BW AO	2005/0108483 A1	05-19-2005	Bungo	

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BW AP	JEONG et al., "Cost Sensitive Cache Replacement Algorithms", June 2002, Second Workshop on Caching, Coherence and Consistency, NY, pp. 1-14.

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/Ben Wang/

Date Considered:

04/04/2007

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Application Number	10806917
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Art Unit	2192
Examiner Name	Dan, Tuan-Guang Ben C. Wang
Attorney Docket Number	AUS920040063US1

U.S. PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
BW	1	6973417	B1	2005-12-06	Maxwell et al.	
BW	2	6721875	B1	2004-04-13	McCormick et al.	

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U.S. PATENT APPLICATION PUBLICATIONS

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CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

☐ That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

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☒ That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).

- ☐ See attached certification statement.
- ☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
- ☐ None

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Gerald H. Glanzman/	Date (YYYY-MM-DD)	2006-08-29
Name/Print	Gerald H. Glanzman	Registration Number	25035

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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	Art Unit	2192	
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U.S.PATENTS							Remove
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	
BW	1	5966538		1999-10-12	Granston et al.		
BW	2	5940618		1999-08-17	Blandy et al.		
BW	3	6189142	B1	2001-02-13	Johnston et al.		
BW	4	5768500		1998-06-16	Agrawal et al.		
BW	5	6026235		2000-02-15	Shaughnessy		
BW	6	5950003		1999-09-07	Kaneshiro et al.		
BW	7	6594820	B1	2003-07-15	Ungar		
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
BW	1	20020073406	A1	2002-06-13	Gove	
BW	2	20030066055	A1	2003-04-03	Spivey	
BW	3	20030131343	A1	2002-07-10	French et al.	
BW	4	20040268316	A1	2004-12-30	Fisher et al.	

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FOREIGN PATENT DOCUMENTS

Examiner Initial*	Cite No	Foreign Document Number ³	Country Code ²	Kind Code ⁴	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T ⁵
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NON-PATENT LITERATURE DOCUMENTS

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EXAMINER SIGNATURE

Examiner Signature	/Ben Wang/	Date Considered	04/04/2007
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Form PTO-1449 LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)			ATTORNEY DOCKET NO. AUS920040063US1		SERIAL NO. 10/806,917	
APPLICANT Dimpsey et al.			FILING DATE March 22, 2004			
GROUP ART UNIT 2122 2192			U.S. PATENT DOCUMENTS			
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/SUBCLASS	FILING DATE	
BW	AA 5,103,394	Apr. 7, 1992	Blasciak	395/575	Dec. 21, 1989	
BW	AB 6,330,662 B1	Dec. 11, 2001	Patel et al.	712/236	Feb. 23, 1999	
BW	AC 6,480,938 B2	Nov. 12, 2002	Vondran, Jr.	711/125	Dec. 15, 2000	
BW	AD 6,430,741 B1	Aug. 6, 2002	Mattson, Jr. et al.	717/154	Feb. 26, 1999	
BW	AE 6,189,141 B1	Feb. 13, 2001	Benitez et al.	717/4	May 4, 1998	
BW	AF 5,930,508	Jul. 27, 1999	Faraboschi et al.	395/706	Jun. 9, 1997	
BW	AG 6,351,844 B1	Feb. 26, 2002	Bala	717/4	Nov. 5, 1998	
BW	AH 6,324,689 B1	Nov. 27, 2001	Lowney et al.	717/9	Sep. 30, 1998	
BW	AI 6,442,585 B1	Aug. 27, 2002	Dean et al.	709/108	Nov. 26, 1997	
BW	AJ 5,774,724	Jun. 30, 1998	Heisch	395/704	Nov. 20, 1995	
BW	AK 5,987,250	Nov. 16, 1999	Subrahmanyam	395/704	Aug. 21, 1997	
BW	AL 6,192,513 B1	Feb. 20, 2001	Subrahmanyam	717/5	Nov. 2, 1998	
BW	AM 5,691,920	Nov. 25, 1997	Levine et al.	364/551.01	Oct. 2, 1995	
BW	AN 6,223,338 B1	Apr. 24, 2001	Smolders	717/4	Sep. 30, 1998	
BW	AO 6,101,524	Aug. 8, 2000	Choi et al.	709/102	Oct. 23, 1997	
BW	AP 6,256,775 B1	Jul. 3, 2001	Flynn	717/4	Dec. 11, 1997	
BW	AQ 6,446,029 B1	Sep. 3, 2002	Davidson et al.	702/186	Jun. 30, 1999	
BW	AR 6,134,676	Oct. 17, 2000	VanHuben et al.	714/39	Apr. 30, 1998	
BW	AS 5,937,437	Aug. 10, 1999	Roth et al.	711/202	Oct. 28, 1996	
BW	AT 6,243,804 B1	Jun. 5, 2001	Cheng	712/228	Jul. 22, 1998	
BW	AU 4,291,371	Sep. 22, 1981	Holtey	364/200	Jan. 2, 1979	
BW	AV 5,938,778	Aug. 17, 1999	John, Jr. et al.	714/45	Nov. 10, 1997	
BW	AW 6,286,132 B1	Sep. 4, 2001	Tanaka et al.	717/4	Jan. 7, 1999	
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BW	BA 5,966,537	Oct. 12, 1999	Ravichandran	395/709	May 28, 1997	
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EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE	
BW	BB	2002/0129309	Sep. 12, 2002	Floyd et al.	714/724	Dec. 18, 2000
BW	BC	2001/0032305	Oct. 18, 2001	Barry	712/34	Feb. 23, 2001
BW	BD	2002/0199179	Dec. 26, 2002	Lavery et al.	717/158	Jun. 21, 2001
BW	BE	2002/0124237	Sep. 5, 2002	Sprunt et al.	717/127	Dec. 29, 2000
BW	BF	2002/0147965	Oct. 10, 2002	Swaine et al.	717/124	Feb. 1, 2001
BW	BG	2002/0019976	Feb. 14, 2002	Patel et al.	717/137	May 25, 2001
FOREIGN PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO	
BW	BH	JP2000029731	Dec. 8, 1999	Japan	G06F 9/38	X
BW	BI	JP2000347863	Dec. 15, 2000	Japan	G06F 9/38	X
OTHER PRIOR ART (including author, title, date, pertinent page, etc.)						
BW	BJ	Kikuchi, "Parallelization Assist System", Joho Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.				
BW	BK	Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.				
BW	BL	Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.				
BW	BM	Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processings Society of Japan, Vol. 36, No. 8, Aug. 1995, pp. 1995-2006.				
BW	BN	Talla et al., "Execution Characteristics of Multimedia Applications on a Pentium II Processor", IEEE International Performance, Computing, and Communications Conference, 19 th , Phoenix, Feb. 20-22, 2000, pp. 516-524.				
BW	BO	IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance Measurement Tool for Software Development Cost Reduction", April 2001, pp. 686-688.				
RELATED PATENT APPLICATIONS						
EXAMINER INITIAL		APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE	
BW	BP	09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug	Nov. 4, 1999	
BW	BQ	08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Processing System	Oct. 2, 1995	
DATE CONSIDERED			04/04/2007			
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RELATED PATENT APPLICATIONS					
EXAMINER INITIAL		APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
BW	BR	10/675,777 / AUS920030477US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses	Sep. 30, 2003
BW	BS	10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003
BW	BT	10/675,831 / AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations	Sep. 30, 2003
BW	BU	10/675,778 / AUS920030480US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold	Sep. 30, 2003
BW	BV	10/675,776 / AUS920030481US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations	Sep. 30, 2003
BW	BW	10/675,751 / AUS920030482US1	DeWitt, Jr. et al.	Method and Apparatus for Debug Support for Individual Instructions and Memory Locations	Sep. 30, 2003
BW	BX	10/675,721 / AUS920030483US1	Levine et al.	Method and Apparatus to Autonomically Select Instructions for Selective Counting	Sep. 30, 2003
BW	BY	10/674,642 / AUS920030484US1	Levine et al.	Method and Apparatus to Autonomically Count Instruction Execution for Applications	Sep. 30, 2003
BW	BZ	10/674,606 / AUS920030485US1	Levine et al.	Method and Apparatus to Autonomically Take an Execution on Specified Instructions	Sep. 30, 2003
BW	CA	10/675,783 / AUS920030486US1	Levine et al.	Method and Apparatus to Autonomically Profile Applications	Sep. 30, 2003
BW	CB	10/675,872 / AUS920030487US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction and Memory Location Ranges	Sep. 30, 2003
BW	CC	10/757,250 / AUS920030488US1	Levine et al.	Method and Apparatus for Maintaining Performance Monitoring Structures in a Page Table for use in Monitoring Performance of a Computer Program	Jan. 14, 2004
BW	CD	10/757,192 / AUS920030543US1	DeWitt, Jr. et al.	Method and Apparatus for Providing Pre and Post Handlers for Recording Events	Jan. 14, 2004
BW	CE	10/757,192 / AUS920030548US1	DeWitt, Jr. et al.	Method and Apparatus for Determining Computer Program Flows Autonomically Using Hardware Assisted Thread Stack Tracking and Cataloged Symbolic Data	March 18, 2004
DATE CONSIDERED 04/04/2007			EXAMINER /Ben Wang/		
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Form PTO-1449 LIST OF PRIOR ART CITED BY APPLICANT <i>(Use several sheets if necessary)</i>		ATTORNEY DOCKET NO. AUS920040063US1		SERIAL NO. 10/806,917	
		APPLICANT Dimpsey et al.			
		FILING DATE March 22, 2004		GROUP ART UNIT 2422 2192	
RELATED PATENT APPLICATIONS					
EXAMINER INITIAL		APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE
BW	CF	10/757,227 / AUS920030555US1	DeWitt, Jr. et al.	Method and Apparatus for Autonomically Moving Cache Entries to Dedicated Storage when False Cache Line Sharing is Detected	Jan. 14, 2004
BW	CG	10/757,197 / AUS920030556US1	DeWitt, Jr. et al.	Method and Apparatus for Optimizing Code Execution Using Annotated Trace Information having Performance Indicator and Counter Information	Jan. 14, 2004
BW	CH	10/807,094 / AUS920040059US1	Dimpsey et al.	Method and Apparatus for Providing Hardware Assistance for Code Coverage	Mar. 22, 2004
BW	CI	10/808,716 / AUS920040060US1	Dimpsey et al.	Method and Apparatus for Autonomic Test Case Feedback Using Hardware Assistance for Code Coverage	Mar. 22, 2004
BW	CJ	10/806,576 / AUS920040061US1	Dimpsey et al.	Method and Apparatus for Hardware Assistance for Data Access Coverage	Mar. 22, 2004
BW	CK	10/806,633 / AUS920040062US1	Dimpsey et al.	Method and Apparatus for Providing Hardware Assistance for Data Access Coverage on Dynamically Allocated Data	Mar. 22, 2004
BW	CL	10/806,871 / AUS920040064US1	Dimpsey et al.	Method and Apparatus for Hardware Assistance for Prefetching Data	Mar. 22, 2004
BW	CM	10/806,866 / AUS920040065US1	Dimpsey et al.	Method and Apparatus for Prefetching Data from a Data Structure	Mar. 22, 2004
DATE CONSIDERED 04/04/2007			EXAMINER /Ben Wang/		
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(Not for submission under 37 CFR 1.99)

Application Number	10806917
Filing Date	2004-03-22
First Named Inventor	Dimpsey et al.
Art Unit	2192
Examiner Name	<u>Dam, Tuan Quang</u> Ben C. Wang
Attorney Docket Number	AUS920040063US1

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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
BW	1	5761103		1998-06-02	Oakland et al.	
BW	2	6549959	B1	2003-04-13	Yates et al.	
BW	3	6772322	B1	2004-08-03	Merchant et al.	
BW	4	5937437		1999-08-10	Roth et al.	
BW	5	6070009		2000-05-30	Dean et al.	
BW	6	6381679	B1	2002-04-30	Matsubara et al.	
BW	7	6687794	B2	2004-02-03	Malik	
BW	8	6539458	B2	2003-03-25	Holmberg	

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BW	9	6973542	B1	2005-12-06	Schmuck et al.	
BW	10	6782454	B1	2004-08-24	Damron	
BW	11	5652858		1997-06-29	Okada et al.	
BW	12	6848029	B2	2005-01-25	Coldewey	
BW	13	5822790		1998-10-13	Mehrotra	

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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
BW	1	"Method for the dynamic prediction of nonsequential memory accesses", September 25, 2002, pp. 1-4, ip.com IPCOM000009888D	<input type="checkbox"/>
BW	2	"Cache-Miss Director - A Means of Prefetching Cache Missed Lines", August 1, 1982, IBM Technical Disclosure Bulletin, Vol. 25, Issue 3A, pp.1286	<input type="checkbox"/>

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CERTIFICATION STATEMENT

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- ☐ See attached certification statement.
- ☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
- ☐ None

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Gerald H. Glanzman/	Date (YYYY-MM-DD)	2006-11-10
Name/Print	Gerald H. Glanzman	Registration Number	25035

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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
BW	1	6542985		2003-04-01	Johnson et al.	
BW	2	4316245		1982-02-16	Luu et al.	
BW	3	7181723		2007-02-20	Luk et al.	
BW	4	6961925		2005-11-01	Callahan, II et al.	
BW	5	5950003		1999-09-30	Kaneshiro et al.	
BW	6	5212794		1993-05-18	Pettis et al.	
BW	7	5659679		1997-08-19	Alpert et al.	
BW	8	6006033		1999-12-21	Heisch	

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Art Unit	2192	
Examiner Name	Wang, Ben C.	
Attorney Docket Number	AUS920040063US1	

BW	9	6119075		2000-09-12	Dean et al.	
BW	10	6189141	B1	2001-02-13	Benitez et al.	
BW	11	6237141	B1	2001-05-22	Holzle et al.	
BW	12	6351844	B1	2002-02-26	Bala	
BW	13	6505292	B1	2003-01-07	Witt	
BW	14	6631514	B1	2003-10-07	Le	
BW	15	5794028		1998-08-11	Tran	
BW	16	5950009		1999-09-07	Bortnikov et al.	
BW	17	5950003		1999-09-07	Kaneshiro et al.	
BW	18	5581778		1996-12-03	Chin et al.	
BW	19	5752062		1998-05-12	Gover et al.	

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BW	20	6446029	B1	2002-09-03	Davidson et al.	
BW	21	5970439		1999-10-19	Levine et al.	

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BW	1	20040194076	A1	2004-09-01	Comp et al.	
BW	2	20020073406	A1	2002-06-13	Gove	
BW	3	20030014741	A1	2003-01-16	Megiddo et al.	
BW	4	20020157086	A1	2002-10-24	Lewis et al.	
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BW	1	RAMIREZ et al., "The Effect of Code Reordering on Branch Prediction", Proceedings of the International Conference on Parallel Architectures and Compilation Techniques, October 2000, pp. 189-198.					<input type="checkbox"/>
BW	2	YANG et al., "Improving Performance by Branch Reordering", Proceedings of the ACM SIGPLAN 1998 Conference on Programming Language Design and Implementation, Montreal Canada, 1008, pp. 130-141.					<input type="checkbox"/>
BW	3	CONTE et al., "Accurate and Practical Profile-Driven Compilation Using the Profile Buffer", Proceedings of the 29th Annual ACM/IEEE International Symposium on Microarchitecture, Paris, France, 1996, pp. 36-45.					<input type="checkbox"/>
BW	4	CONTE et al., "Using Branch Handling Hardware to Support Profile-Driven Optimization", Proceedings of the 27th Annual International Symposium on Microarchitecture, San Jose CA, 1994, pp. 12-21.					<input type="checkbox"/>
BW	5	FISHER, "Trace Scheduling: A Technique for Global Microcode Compaction", IEEE Transactions on Computers, Vol. C30, No. 7, July 1981, pp. 478-490.					<input type="checkbox"/>
BW	6	CHANG et al., "Using Profile Information to Assist Classic Code Optimizations", Software Pract. Exper. 21, December 1991, pp. 1301-1321.					<input type="checkbox"/>
BW	7	SCHMIDT et al., "Profile-Directed Restructuring of Operating System Code", IBM Systems Journal, 1998, Vol. 37, No. 2, pp. 270-297.					<input type="checkbox"/>
BW	8	AHO et al., "Compilers: Principles, Techniques, and Tools", Addison-Wesley, 1988, pp. 488-497.					<input type="checkbox"/>

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9	INTEL, "Intel IA-64 Architecture Software Developer's Manual", Revision 1.1, Vol. 4, No. 245320.002, July 2001.	<input type="checkbox"/>
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Signature	/Gerald H. Glanzman/	Date (YYYY-MM-DD)	2007-03-29
Name/Print	Gerald H. Glanzman	Registration Number	25,035

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